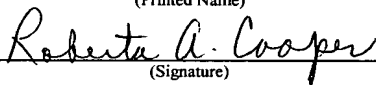




Atty. Dkt. No. 039153-0642 (H0970)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Paton et al.  
Title: OFFSET SPACER SMOS  
PROCESS FOR FORMING  
N-TYPE TRANSISTORS  
Appl. No.: 10/619877  
Filing Date: 07/15/2003  
Examiner: Jeff B. Vockrodt  
Art Unit: 2822  
Confirmation No.: 7818

<b>CERTIFICATE OF EXPRESS MAILING</b>	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.	
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Roberta A. Cooper	
(Printed Name)	
	
(Signature)	

**AMENDMENT AND REPLY UNDER 37 CFR 1.111**

Mail Stop NON-FEE AMENDMENT  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

This communication is responsive to the Non-Final Office Action dated March 29, 2004, concerning the above-referenced patent application.

**Amendments to the Specification** begin on page 2 of this document.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this document.

**Remarks/Arguments** begin on page 7 of this document.

Please amend the application as follows: